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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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David A. Fish

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PHILIPS INTELLECTUAL PROPERTY & STANDARDS

P.O. BOX 3001

BRIARCLIFF MANOR, NY 10510

EXAMINER

CRAWLEY, KEITH L

ART UNIT

PAPER NUMBER

2629

MAIL DATE

DELIVERY MODE

04/22/2010

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/569,177	Applicant(s) FISH, DAVID A.	
	Examiner KEITH CRAWLEY	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 04 March 2010.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 16-32 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 16, 17, 19-21 and 26-29 is/are rejected.
- 7) ☒ Claim(s) 18, 22-25, 30-32 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Double Patenting

The terminal disclaimer filed 2/3/10 has been approved and the provisional nonstatutory obviousness-type double patenting rejection of claims 17-22, 24-27, and 29-30 is withdrawn.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 16-17, 21, 26, and 28-29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Inoue et al. (US 7,071,635) in view of Jo (US 7,319,444).

Regarding claim 16, Inoue discloses a display device comprising an array of light emitting display elements arranged in rows and columns (fig. 1, organic EL display 2, see fig. 8),

with a plurality of pixels in a column being supplied with current from a respective column power supply line (fig. 8, drive line 4, see col. 2, line 63)

and the pixels being addressed row by row, the addressing of all rows defining a field period (col. 2, line 8-12, gate driver successively applies voltages to scanning electrodes, defining this as a field period is well known in the art),

the device further comprising: compensation circuitry for modifying target pixel drive currents corresponding to desired pixel brightness levels to take account of the voltage on the column power supply line at each pixel resulting from the currents drawn from the column power supply line by the plurality of pixels in the column being supplied by the column power supply line for each row addressing cycle in a field period (figs. 1 and 2, see col. 3, line 44-59)

and the dependency of pixel brightness characteristics on a voltage on a row conductor at the pixel (col. 5, line 36-41 and col. 7, line 10-25, current supplied to EL element is controlled according to the voltage on gate of TR2, voltage-current relationships are referenced in compensation scheme; see fig. 8, gate voltage of TR2 is controlled according to the voltage on gate of TR1 via row conductor, see col. 2, line 1-14).

Inoue fails to disclose taking account of changes in the drain-source voltage of the drive transistor.

Jo teaches taking account of changes in the drain-source voltage of the drive transistor (fig. 4, see col. 7, line 56-62, see also col. 8, line 3-33, current flowing through organic EL element is modified when the voltage between the source and drain of drive TFT 1102 is reduced due to degradation of organic EL element).

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Inoue and Jo are both directed to methods and devices for driving organic EL displays utilizing correction circuits. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the display of Inoue with the correction circuit of Jo since such a modification provides a display which prevents degradation of quality of a displayed image (Jo, col. 1, line 51-58).

Regarding claim 17, Inoue discloses wherein the compensation circuitry comprises: means for applying an algorithm to the target pixel drive currents which represents a relationship between the currents drawn by the plurality of pixels in a column and the voltages on the column power supply line at the locations of the pixels (figs. 1 and 2, see col. 3, line 44-59)

and the dependency of the pixel brightness characteristics on the voltage on the row conductor (col. 5, line 36-41 and col. 7, line 10-25, current supplied to EL element is controlled according to the voltage, voltage-current relationships are referenced in compensation scheme).

Regarding claim 21, Inoue discloses wherein the algorithm uses a value including a term derived from a resistance of the column power supply line (fig. 5, resistance R_0 and R of drive line 4, see col. 5, line 42-51 and mathematical expressions 1 and 2).

Regarding claim 26, Inoue discloses wherein the means for applying an algorithm comprises a look up table (fig. 2, lookup table 31, see col. 7, line 10-18).

Regarding claim 28, this claim is rejected under the same rationale as claim 16.

Regarding claim 29, this claim is rejected under the same rationale as claim 17.

3. Claims 19-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Inoue in view of Jo as applied to claim 17 above, and further in view of Kawashima et al. (US 6,091,203).

Regarding claim 19, Inoue discloses wherein the algorithm uses a value including terms derived from: the voltage-current characteristics of the drive transistor and the voltage-current characteristics of the light emitting display element (col. 5, line 36-41 and col. 7, line 10-25, current supplied to EL element is controlled according to the voltage, voltage-current relationships are referenced in compensation scheme);

Inoue in view of Jo fails to disclose wherein each of the plurality of pixels in a column comprises a current sampling transistor which samples an input current and provides a drive voltage to a drive transistor.

Kawashima teaches wherein each of the plurality of pixels in a column comprises a current sampling transistor which samples an input current and provides a drive voltage to a drive transistor (fig. 2, conversion TFT 18 samples current and provides

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drive voltage to drive TFT 15, see col. 8, line 10-27, see also fig. 6 and col. 10, line 26-30).

Inoue in view of Jo and Kawashima are both directed to methods and devices for driving active matrix displays. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the method and device of Inoue in view of Jo with the method and device of Kawashima since such a modification provides an element driving device capable of controlling the operation of active elements at a desired state (Kawashima, col. 3, line 23-26) and provides a flat display device in which crosstalk is suppressed (Inoue, col. 2, line 41-43).

Regarding claim 20, Inoue discloses wherein the drive transistor and the light emitting display element of each of the plurality of pixels in a column are in series between the column power supply line and a common line (fig. 5, drive transistor TR2 and EL element 20 are in series between drive line 4 and counterelectrode).

4. Claim 27 is rejected under 35 U.S.C. 103(a) as being unpatentable over Inoue in view of Jo as applied to claim 26 above, and further in view of Cok (US 7,164,417).

Regarding claim 27, Inoue in view of Jo fails to disclose at least one pixel compensation module, and means for updating the values of the look up table to enable changes in pixel brightness characteristics over time to be modeled based on analysis of the characteristics of the pixel compensation module.

Cok teaches at least one pixel compensation module (fig. 1, light emitting element 17 and photosensor 15, see col. 2, line 54-60),

and means for updating the values of the look up table to enable changes in pixel brightness characteristics over time to be modeled based on analysis of the characteristics of the pixel compensation module (fig. 1, col. 2, line 37-53, see also col. 3, line 33-42 and col. 2, line 27-29).

Inoue in view of Jo and Cok are both directed to methods and devices for driving active matrix displays. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the method and device of Inoue in view of Jo with the method and device of Cok since such a modification provides a simple design for accommodating optical feedback from active matrix display devices (Cok, col. 1, line 66-67) and provides a flat display device in which crosstalk is suppressed (Inoue, col. 2, line 41-43).

Allowable Subject Matter

1. Claims 18, 22-25, and 30-32 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

2. Applicant's arguments with respect to claims 16 and 28 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to KEITH CRAWLEY whose telephone number is (571)270-7616. The examiner can normally be reached on M-F, 7:30-5:00 EST, alternate Fri. off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on (571)272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Bipin Shalwala/
Supervisory Patent Examiner, Art Unit 2629

/KEITH CRAWLEY/
Examiner, Art Unit 2629